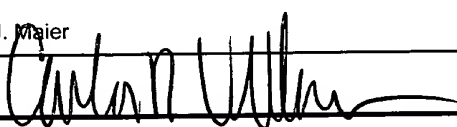


UTILITY PATENT APPLICATION TRANSMITTAL <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No.	0057-2533-2YY CONT
	First Inventor or Application Identifier	Hideki TAKAHASHI
	Title	INSULATED GATE SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

511 U.S. PTO
09/421217
10/20/99

APPLICATION ELEMENTS <small>See MPEP chapter 600 concerning utility patent application contents</small>	ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
<p>1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)</p> <p>2. <input checked="" type="checkbox"/> Specification Total Pages 48</p> <p>3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets 22</p> <p>4. <input checked="" type="checkbox"/> Oath or Declaration Total Pages 3</p> <p>a. <input type="checkbox"/> Newly executed (original or copy)</p> <p>b. <input checked="" type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 15 completed)</p> <p>i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).</p> <p>5. <input checked="" type="checkbox"/> Incorporation By Reference (usable if box 4B is checked) The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4B, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein.</p>	ACCOMPANYING APPLICATION PARTS <p>6. <input checked="" type="checkbox"/> Assignment recorded at Reel/Frame 7851/0231</p> <p>7. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement (when there is an assignee) <input type="checkbox"/> Power of Attorney</p> <p>8. <input type="checkbox"/> English Translation Document (if applicable)</p> <p>9. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations</p> <p>10. <input checked="" type="checkbox"/> Preliminary Amendment with Attachment (1 page)</p> <p>11. <input checked="" type="checkbox"/> White Advance Serial No. Postcard</p> <p>12. <input type="checkbox"/> Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application. Status still proper and desired.</p> <p>13. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed)</p> <p>14. <input checked="" type="checkbox"/> Other: Request for Priority</p>
<p>15. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:</p> <p><input checked="" type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.: 08/937,163</p> <p>Prior application information: Examiner: Hack Group Art Unit: 2812</p>	
<p>16. Amend the specification by inserting before the first line the sentence:</p> <p><input checked="" type="checkbox"/> This application is a <input checked="" type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP)</p> <p>of application Serial No. 08/937,163 Filed on September 25, 1997, now allowed, which is a divisional of application serial no. 08/566,572, filed November 28, 1995, now issued as U.S. Patent 5,751,024.</p> <p><input type="checkbox"/> This application claims priority of provisional application Serial No. Filed</p>	
<p>17. CORRESPONDENCE ADDRESS</p> <p>OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. FOURTH FLOOR 1755 JEFFERSON DAVIS HIGHWAY ARLINGTON, VIRGINIA 22202 (703) 413-3000 FACSIMILE: (703) 413-2220</p>	

Name:	Gregory J. Maier	Registration No.:	25,599
Signature:		Date:	10/20/99
Name:	Carlos R. Villamar	Registration No.:	43,224

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hideki TAKAHASHI

SERIAL NO: New Application

FILING DATE: Herewith

FOR: INSULATED GATE SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231


FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	18 - 20 =	0	× \$18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	× \$78 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$260 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEE				\$760.00
TOTAL OF ABOVE CALCULATIONS				\$760.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
<input type="checkbox"/> RECORDATION OF ASSIGNMENT			+ \$40 =	\$0.00
TOTAL				\$760.00

☐ Please charge Deposit Account No. 15-0030 in the amount of

A duplicate copy of this sheet is enclosed.

☒ A check in the amount of \$760.00 to cover the filing fee is enclosed.☒ The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030.
A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Gregory J. Maier

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hideki TAKAHASHI

SERIAL NO: New Application

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JCS11 U.S. PTO
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FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	18 - 20 =	0	× \$18 =	\$0.00
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TOTAL				\$760.00

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- ☒ A check in the amount of \$760.00 to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

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(OSMMN 11/98)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hideki TAKAHASHI

GAU: To be Assigned

SERIAL NO: New Application

EXAMINER: To be Assigned

FILED: Herewith

FOR: INSULATED GATE SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

REQUEST FOR PRIORITY

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

- ☒ Full benefit of the filing date of U.S. Application Serial Number 08/937,163, filed September 25, 1997, is claimed pursuant to the provisions of 35 U.S.C. §120.
- ☐ Full benefit of the filing date of U.S. Provisional Application Serial Number , filed , is claimed pursuant to the provisions of 35 U.S.C. §119(e).
- ☒ Applicants claim any right to priority from any earlier filed applications to which they may be entitled pursuant to the provisions of 35 U.S.C. §119, as noted below.

In the matter of the above-identified application for patent, notice is hereby given that the applicants claim as priority:


<u>COUNTRY</u>	<u>APPLICATION NUMBER</u>	<u>MONTH/DAY/YEAR</u>
Japan	7-054564	3/14/95

Certified copies of the corresponding Convention Application(s)


- ☐ are submitted herewith
- ☐ will be submitted prior to payment of the Final Fee
- ☒ were filed in prior application Serial No. 08/566,572 filed November 28, 1995.
- ☐ were submitted to the International Bureau in PCT Application Number .
Receipt of the certified copies by the International Bureau in a timely manner under PCT Rule 17.1(a) has been acknowledged as evidenced by the attached PCT/IB/304.
- ☐ (A) Application Serial No.(s) were filed in prior application Serial No. filed ; and
(B) Application Serial No.(s)
 - ☐ are submitted herewith
 - ☐ will be submitted prior to payment of the Final Fee

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


Gregory J. Maier
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